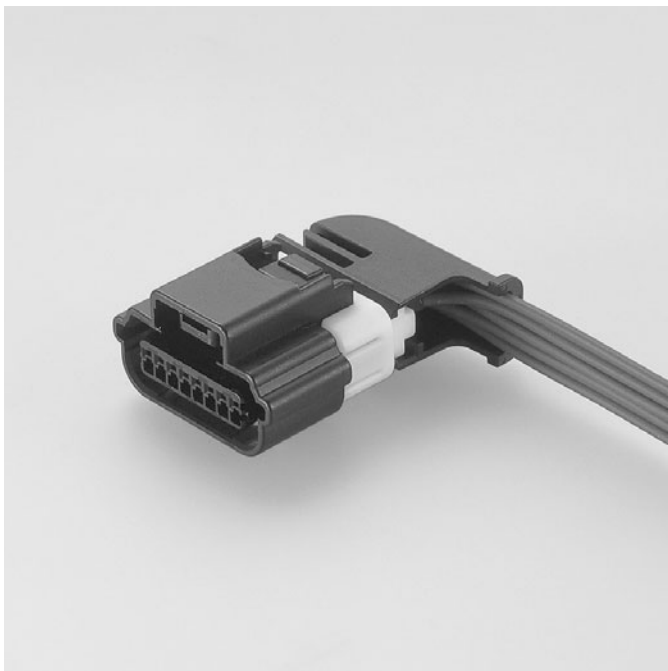


ATSSCONNECTOR

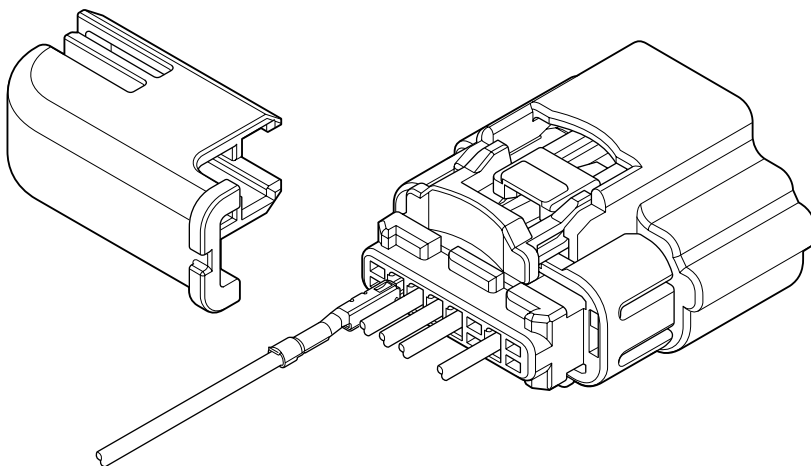
Sealed connectors/Wire-to-wire



Features

- 0.64 Sealed Connector
- Compliant with the Standard USCAR Footprints
- (Optional) Connector Position Assurance (CPA), Independent Security Lock Mechanism
- Optional Cable Cover available for Cable routing
- Variety of Cavity block options for unused Circuits

0.64 Sealed Connector compliant with the Standard USCAR Footprints.

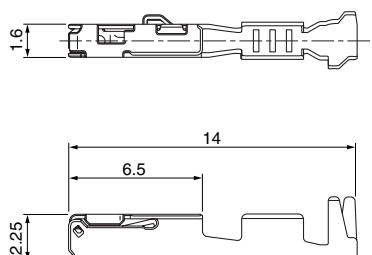


Specifications

- Current rating: 5A DC max.
- Voltage rating: 14V DC
- Temperature range: -40°C to +125°C
(including temperature rise in applying electrical current)
- Contact resistance: Initial value/15mΩ max.
After environmental testing/15mΩ max.
- Insulation resistance: 100MΩ min.
- Applicable wire: SAE/ AVSS/ CAVS
0.3mm² to 0.8mm²

- * Compliant with ELV/RoHS.
- * Contact JST for details.

Female terminal

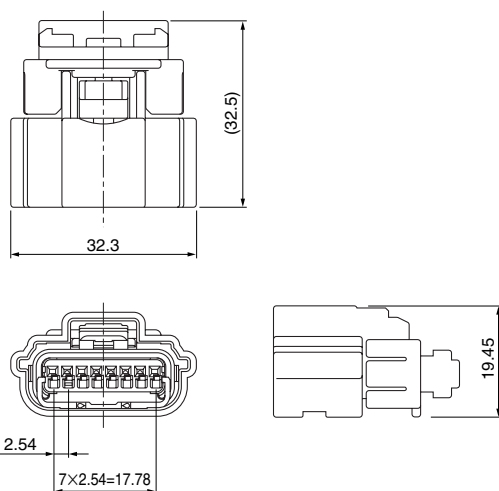


Model No.	Applicable wire range		Q'ty/reel
	Conductor (mm ²)	Insulation O.D. (mm)	
①SAIT-A03T-M064	0.3 to 0.5	≤1.9	6,500
①SAIT-A02T-M064	0.75 to 0.8	≤1.9	6,500
②SAIT-A03GF-M064	0.3 to 0.5	≤1.9	6,500
②SAIT-A02GF-M064	0.75 to 0.8	≤1.9	6,500

Material and Finish

- ①Copper alloy, tin-plated (reflow treatment)
- ②Copper alloy, Contact area; gold-plated
Barrel area; tin-plated

Female connector



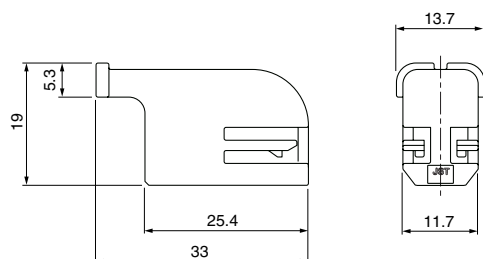
Circuits	CPA	Model No.	Q'ty/box
8	YES	ATSSPB-C08()-1AK	75
	NO	ATSSPB-A08()-1AK	75

Material

Housing: Glass-filled PBT, black
TPA: Glass-filled PBT, natural (white)
CPA: Glass-filled PBT, red
Back cover: Glass-filled PBT
Ring seal: Silicone, green
Matte seal: Silicone, green

Note: Circuits from 3 to 8 available with flashed over back option other than abovementioned housing. Contact JST for details.

Cable cover



Circuits	Model No.	Q'ty/box
8	ATSSCC-08-K	150

Material

Glass-filled PBT, black

Crimping machine, Applicator

Strip terminal	Crimping machine	Crimp applicator MKS-L	
		Dies	Crimp applicator with dies
SAIT-A03()-M064	AP-K2N	MK/SAIT-A03-064	APLMK SAIT-A03-064
SAIT-A02()-M064		MK/SAIT-A02-064	APLMK SAIT-A02-064

Note: When crimping operation is conducted using an applicator and die set other than the above, JST cannot guarantee the performance of the terminal.